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PTO/SB/21 (01-03)
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	Filing Date	March 01, 2002
	First Named Inventor	Atul V. Ghia
	Examiner Name	Anh Q. Tran
	Art Unit	2819
	Patent No.	
Attorney Docket Number		X-1061 US

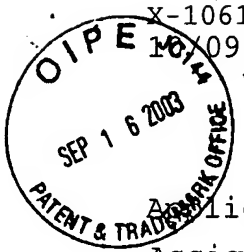
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PATENT
Conf. No. 6539

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IN THE UNITED STATES PATENT OFFICE

Applicants: Atul V. Ghia et al.
Assignee: Xilinx, Inc.
Title: Low Jitter Clock for a Physical Media Access
Sublayer on a Field Programmable Gate Array

Serial No.:	10/090,239	File Date:	03-01-02
Examiner:	Anh Q. Tran	Art Unit:	2819
Docket No.:	X-1061 US	Conf. No.:	6539

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AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION

Dear Sir:

In response to the First Office Action mailed from the
Patent Office on June 16, 2003, please replace/substitute the
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